

## High Level Synthesis From Algorithm To Digital Circuit

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**VLSI Design [Module 01 - Lecture 02] High Level Synthesis: High-level Synthesis (HLS) flow** Course: Optimization Techniques for Digital VLSI Design Instructor: Dr. Chandan Karfa Department of Computer Science and ...

**High-Level Synthesis (HLS) theory** Description of what **High-Level Synthesis** is and its main steps.

**Introduction to High-Level Synthesis (Part 1 of 7)** In the class, you will learn how to use the Intel® HLS Compiler to synthesize and verify IP components for Intel FPGAs. We ...

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## ***High Level Synthesis Explanation***

***Getting started with Vivado High Level Synthesis*** Learn how to use the GUI interface to create a Vivado HLS project, compile and execute your C, C++ or SystemC **algorithm**, ...

***Vivado High Level Synthesis - Image Processing Algorithm Demonstration*** Vivado **High-Level Synthesis** accelerates design implementation by enabling C, C++ and System C specifications to be directly ...

***Implementation of Object Tracking Algorithm on ZYNQ Platform using High-Level Synthesis*** Graduate Project Demonstration By Uzaif Sharif  
Department of Electrical and Computer Engineering  
California State University ...

***What Is HLS?*** A presentation that discusses what **High-Level Synthesis** is and is not. A very good introduction for those new to the subject.

***Moving Between FPGA and ASIC with High-Level Synthesis -- Mentor*** Writing RTL that works smoothly on both FPGA and ASIC implementations is nearly impossible. But, **High-Level Synthesis** (HLS) ...

***Mod-01 Lec-03 Transformations for High Level Synthesis*** Design Verification and Test of Digital VLSI Circuits by Prof. Jatindra Kumar Deka, Dr. Santosh Biswas, Department of Computer ...

***High Level Synthesis (HLS) Explanation 2: Scheduling*** In this video I explain Zhang's SDC scheduling **algorithm** using a simple example HLS program. Original Zhang scheduling paper: ...

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**High Level Synthesis (HLS) Explanation 15: Whats wrong with the dumb algorithm?** In this video I talk about what is wrong with the dumb **algorithm** for finding initiation interval constraints from the last video.

**Video 0: Floating-point C++ Algorithm to Optimized RTL Implementation Using Catapult HLS** This video provides an introduction to the Edge Detection Walkthrough video series and an overview of the Catapult **High-Level** ...

**High Level Synthesis (HLS) Explanation 1** In this video I use an open source HLS program I've been working on to explain the structure of HLS compilers that translate ...

**Qt-based open source High-Level Synthesis Design Space Exploration framework** In this video we introduce an open source Qt-based High-Level Synthesis Design Space Exploration framework developed @DARClab

## **High Level Synthesis and Timing Issues**

**HLS Loop Optimizations (Part 3 of 7)** In the class, you will learn how to use the Intel® HLS Compiler to optimize loops in your HLS component. We will cover the ...

**High-Level Synthesis Design Space Explorer** Final year project of a HLS Design Space Explorer with a Qt-based GUI.

**Vivado HLS Example: FFT** A walkthrough of implementing an FFT module using Vivado HLS. Does not cover the process of designing the module, but does ...

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